PERSONAL DETAILS AND AFFILIATION

Name of the Faculty Member	Kalieswari C
Designation	Assistant Professor
Department	Electronics and Communication Engineering
Date of joining the institution	15.05.2019
Date of Birth	18.05.1991

EDUCATIONAL QUALIFICATION

S.NO.	QUALIFICATION	INSTITUTION STUDIED	UNIVERSITY / BOARD	YEAR OF PASSING
1	B.E. (ECE)	RVS College of Engineering and Technology, Dindigul	Anna University	2012
2	M.E./M.Tech.	RMK Engineering College, Chennai	Anna University	2014

EXPERIENCE

S.NO.	NAME OF THE INSTITUTION / ORGANIZATION	POSITION HELD	FROM	TO	EXPERIENCE Y	M
1	National Engineering College, Kovilpatti	Assistant Professor	15.05.2019	Till Date	-	2
2	Dr.Sivanthi Aditanar College of Engineering, Tiruchendur	Assistant Professor	02.01.2015	07.11.2017	2	10

LIST OF INTERNATIONAL JOURNAL PUBLICATIONS

1	K.G.Shanthi, Dr.N.Nagarajan, **C.Kalieswari** (2014). FPGA realization of high speed FIR filter based on Distributed Arithmetic. International Journal of Engineering andTechnology, Volume 6, No3, Pages 1407 -1414, Jun-July 2014 . [IF: 0.178]
2	Kalieswari C, A. Joe (2016). Multiple Fusion Based Single Image Dehazing. International Journal of Recent Research and Applied Studies, ISSN: 2349 – 4891 Volume 3, Issue 3(13), Pages 51-55, 2016. [IF: 2.665]

LIST OF INTERNATIONAL CONFERENCE PUBLICATIONS

S.NO.	TITLE	PERIOD	NAME OF THE PROGRAMME & VENUE
1	Systolic based high speed and area efficient FIR architecture using distributed arithmetic	28.03.2017	NCEA2017 & P.S.R.Rengasamy College of Engineering for Women, Sivakasi
2	Distributed arithmetic based high throughput and hardware efficient FIR architecture	24.03.2017	NCRTE2C2T-2K17 & Universal College of Engineering and Technology.
3	Low Delay and Hardware Efficient Implementation of Distributed Arithmetic Based FIR Filter	12.04.2014	ICRTIT 2014 & Madras Institute of Technology, Anna University
4	High throughput and Area efficient FPGA implementation of FIR filter using distributed arithmetic	20.02.2014	ICACT2014 & JKKN College of Engineering and Technology

WORKSHOPS/SEMINARS /TRAINING PROGRAMMES ATTENDED

S.NO.	NAME OF THE PROGRAMME & VENUE	DATES WITH DURATION
1	National workshop on"Controllers and Communication System Design using LABVIEW" at Dr.Sivanthi Aditanar College of Engineering	01.04.2017
2	New Faculty Induction workshopt Dr.Sivanthi Aditanar College of Engineering	8/07/2016 & 09/07/2016
3	Faculty Development Programme on"Analog,MCU,Webench and connectivity" by Texas Instruments,Banglore at Mepco Schlenk Engineering College	15/06/2016 & 18/06/2016
4	National workshop"Signal and Image Processing using MATLAB", Dr.Sivanthi Aditanar College of Engineering	26/09/2016
5	"Industry oriented workshop" conducted by Mistral Solutions	10/12/2015.
6	Faculty development program on "Embedded Systems using ARM7 Micro Controller", Dr.Sivanthi Aditanar College of Engineering	01/12/2015 & 02/12/2015
7	Two days national workshop on "ARM7 with Hands on Training", Dr.Sivanthi Aditanar College of Engineering	29/09/2015 & 30/09/2015
8	One-day workshop on "Matlab" held at Anna University, Chennai	12/03/2013
9	Two days workshop on "VLSI Design using Cadence EDA Tools" organized by Cadence Design Systems India Pvt. Ltd	11/12/2012 & 12/12/2012
10	Two days workshop on "Android" at VIT University, Vellore	30/07/2011 & 31/07/2011

SPECIFIC ACHIEVMENTS (IF ANY)

S.NO.	
1	Secured 13th university rank in post-graduation
2	Secured 338 score in GATE 2015
3	Secured 340 score in GATE 2013.

MEMBERSHIP DETAILS	
S.NO.	
1	Life time member of ISTE (Indian Society for technical education)

SIGNATURE